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## Design and Analysis of a Compact Slotted Microstrip Patch Antenna for 5g Nr Mm Wave Frequency Band

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**Abstract**— 5G wireless communication brings us high speed data transfer between the transmitter and receiver through wireless channel. Data rate of the system will be increased when using mm wave frequency band. Although the increases in the data rate with the increases in the operating frequency leads to path loss. The antenna plays an important role in the mm wave communication; it should be compact enough to fit with other electronic circuits. The device compactness also one of the major problems while implementing the 5G mm wave communication. In this paper, a compact slotted microstrip patch antenna has been designed using Arlon Di clad 880 substrate materials, having a dielectric constant of 2.2. This substrate material is manufactured with woven fiberglass reinforced PTFE laminates which is suitable for high frequency applications. Also, it possess excellent chemical resistant, less moisture absorption and dimensionally stable. The simulated microstrip patch antenna comprises the overall size of  $5 \times 4.5 \times 0.127$  mm (without slot) and  $3.5 \times 4 \times 0.127$  mm (with slot). The simulated results show that the microstrip patch antenna has a return loss of  $-40.7$  dB (without slot) and  $-40$  dB (with slot respectively). Further, a  $5 \times 20$  array will be developed for the massive MIMO applications.

**Index Terms**— Slotted microstrip patch antenna, 28 GHz frequency band, Compact size, 5G Wireless Communications.

### INTRODUCTION

The rapid increases in the usage of cellular devices, the increased data traffic, high speed data transfer and quality of services [11] leads to the introduction of the next generation wireless communication. 5G the next generation wireless communication will satisfy the huge data traffic and connect even more wireless devices with increased data rate [15]. The upcoming technologies and devices such as IoT communication, video streaming, live broadcasting, home automations, IP Cameras etc. that are directly connected to the internet through wireless channel. So, the next generation wireless communication should fulfill the availability and connectivity of devices with increased data rate. The high speed connectivity requires enormous bandwidth. This can be obtained with the increases in the operating frequency of the available and as well as upcoming devices. The FCC 5G spectrum are characterized into three band they are Low Band ( $<1$ GHz), Mid Band (1-6GHz) and High Band (6-100GHz mm wave communication) [13] in order to move the available and upcoming devices from 1 to 5 GHz frequency band. The devices like GPS, Wi-Fi, Bluetooth, up to 4<sup>th</sup> generation wireless communications use less than 6 GHz frequency band, this available 1 to 5 GHz frequency bands [16] getting crowded day by day and leads to interference and achieving the high data and high speed communication is not possible when using this frequency band. So, The FCC new radio frequency spectrum is useful for the 5G wireless communications. Particularly, the 28 GHz frequency bands are popular in the new radio frequency band of FCC. As this frequency band has been considered as high frequency mm band, this band can provided higher data rate to fulfill the requirements of the upcoming devices. Though, the mm wave frequency band has some advantages but it has some

drawbacks too. The path loss is one of the major problems in the mm wave wireless communications [14]. To overcome these problems, a high directional antenna is required and it should be compact in size. The antenna should be easily installed alongside other electronic components of wireless devices. The antenna plays an important role in the wireless communication, without these radiating elements; the wireless communication is not possible. So, the designed antenna should be capable of handling the high data rate and should possess extremely low losses when particularly for the 5G communication because the quality of services are important aspects of the end user devices. Future devices are much more compact to handle, the antenna should be designed accordingly. Most probably the compact wireless devices uses microstrip patch antenna as a radiating elements. The microstrip patch antenna consists of radiating patch, substrate and the ground plane.

## RELATED WORKS

In the existing work [1], a microstrip patch antenna was designed in the operating frequency of 28 GHz; the size of the antenna was  $4.5 \times 5.5 \times 0.4$  mm. Though the size meets the specification but the return loss is not appreciable, the return loss was -17.4dB. In the related works [2], the size of the antenna was  $6.28 \times 7.235 \times 0.5$  mm and, obtained return loss of -13.48 dB and the bandwidth of 847 MHz. Further [4], the microstrip patch antenna was designed with the size of  $6.20 \times 8.40 \times 1.57$  mm. Though, this work has excellent bandwidth but poor VSWR and the increased thickness of an antenna. From the analysis, it is clearly viewed that compensating one parameter will degrade the two or more parameters such as gain will be decreased if the size of an antenna reduced and the bandwidth of an antenna will be increased by increasing the substrate thickness of a microstrip patch antenna. Selecting substrates is also an important task when designing an antenna. The substrates should have extremely low dielectric loss and reduced thickness to design a compact microstrip patch antenna. The most challenging task is the size reduction of an antenna and the tradeoff should be taken to compensate other parameters such as bandwidth, gain and return loss. This paper focuses on the size reduction of a microstrip patch antenna. The antenna was designed and simulated using Arlon Di clad 880 substrate material having a dielectric constant of 2.2 and loss tangent of 0.0009. The size of the designed antenna  $5 \times 4.5 \times 0.254$  mm, which is extremely compact to fit with other electronic components. The Arlon Di clad 880 substrate material was made up of woven reinforced PTFE laminates this will provide a greater dimensional stability than non-woven reinforced PTFE laminates, it has excellent chemical resistance and electrical properties are highly uniform across frequency and water absorption percentage was 0.02.

## DESIGN METHODOLOGY

### A. *Microstrip patch antenna*

Generally, a microstrip patch antenna is called low profile antenna. It consists of a radiating patch and the top, ground plane at the bottom and the substrate material is lies between these planes. The conducting material or radiating patch may be a gold, silver or copper. Copper was used as a radiating patch in this research, when compared to silver and gold, a copper is a low profile. The substrate material was Arlon Di clad 880 has a dielectric constant of 2.2.

### B. Design equations

Before going into the design, the microstrip patch antenna should meet the design considerations [12] such as dielectric substrate height should lies between  $0.003\lambda \leq h \leq \lambda$ , where  $\lambda$  is the free space wavelength. The permittivity of the substrate should lies between  $2.2 \leq \epsilon_r \leq 12$ .

The following equations help to calculate the width and length of the patch.

$$W = \frac{C}{2F} \sqrt{\frac{2}{\epsilon_r + 1}} \quad (1)$$

To calculate the length of the patch, an effective dielectric constant of the substrate and  $\Delta L$  should be known.

$$\epsilon_{reff} = \frac{\epsilon_r + 1}{2} + \frac{\epsilon_r - 1}{2} \sqrt{1 + \frac{12h}{W}} \quad (2)$$

$$\Delta L = 0.412h \frac{(\epsilon_{reff} + 0.8) \left(\frac{W}{h} + 0.264\right)}{(\epsilon_{reff} - 0.258) \left(\frac{W}{h} + 0.8\right)} \quad (3)$$

$$L = \frac{\lambda_s}{2} - 2\Delta L \quad (4)$$

$$\lambda_s = \frac{\lambda}{\sqrt{\epsilon_{reff}}} \quad (5)$$

Where W = Width of the patch material

L = Length of the patch material

h = Height of the substrate material

$\epsilon_r$  = Dielectric Constant of the substrate material

$\epsilon_{reff}$  = Effective dielectric constant of the substrate material

$\Delta L$  = Variable length of the patch material

C = Velocity of light in free space

F = Resonant Frequency

$\lambda_s$  = Wavelength of the substrate material

$\lambda$  = Wavelength of the operating frequency

The below equations gives the dimensions of the substrate material:

$$L_s = L + 6h \quad (6)$$

$$W_s = W + 6h \quad (7)$$

Where  $L_s$  = Length of the substrate material,

$W_s$  = Width of the substrate material,

$L$  = Length of the patch material and

$H$  = Height of the substrate material.

**A. Feeding structure**

Choosing an appropriate feeding will enhance the performance of an antenna. In feeding, contact and non-contact methods exist. In contact feeding such as coaxial and microstrip line feeding are most popular whereas in non-contacting feeding, aperture coupled and proximity coupled feeding techniques are available in the existing system. This research mainly focuses on size reduction. So, a direct feeding technique is used in the design. In existing model, a quarter wave transformer matching network was used for the feeding but it is not compact as it increases the length of the antenna for perfect matching. So, an inset feeding is preferred in this research as it reduces the size of the microstrip patch antenna design.

**Antenna Design Parameters**

**B. Antenna Design 1**

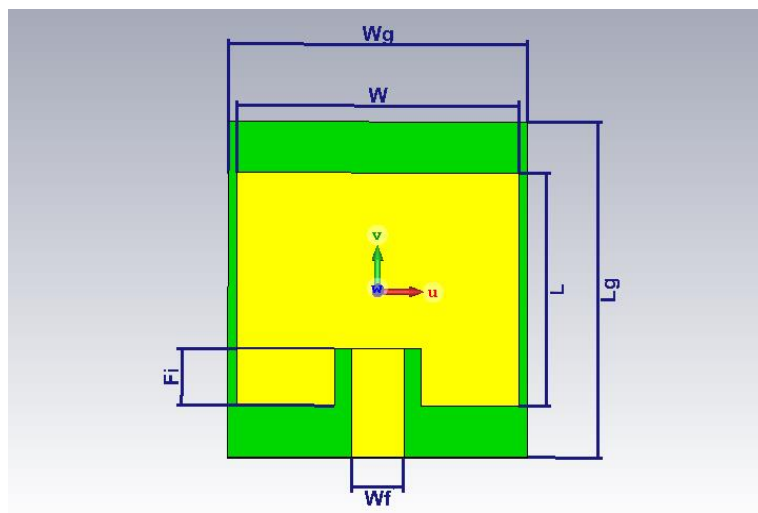
The proposed microstrip antenna is operating under 28 GHz frequency band. The theoretical calculation and optimized values of the proposed microstrip patch antenna were listed in the table I and table II.

**Table I. Calculated Dimension of the Proposed Antenna**

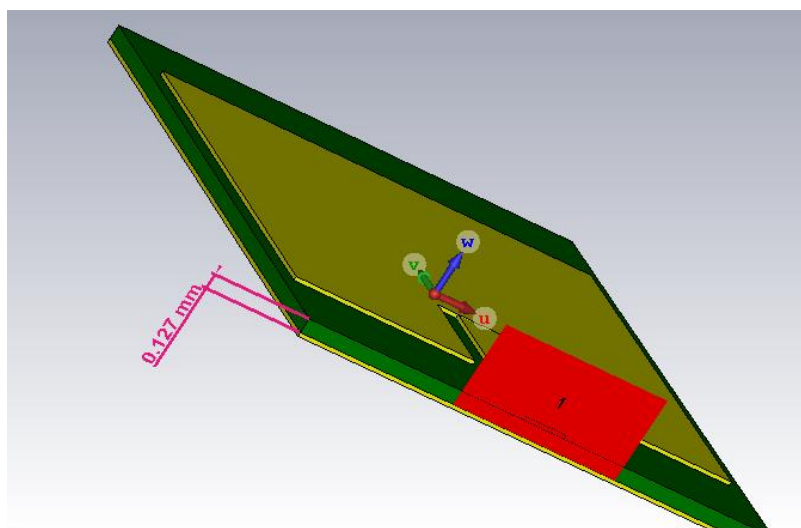
Parameters	Dimensions (mm)
Ground plane length ( $L_g$ )	4.9931
Ground plane Width ( $W_g$ )	5.7591
Patch length ( $L$ )	3.469
Patch Width ( $W$ )	4.235
Substrate length ( $L_s$ )	4.9931
Substrate Width ( $W_s$ )	5.7591
Feed line width ( $W_f$ )	0.39

**Table II. Optimized Dimension of the Proposed Antenna**

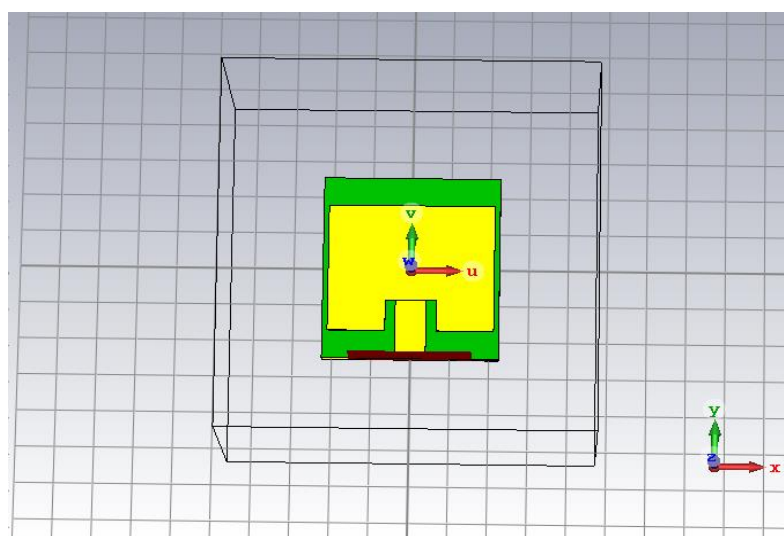
Parameters	Dimensions (mm)
Ground plane length ( $L_g$ )	5
Ground plane Width ( $W_g$ )	4.5
Patch length ( $L$ )	3.5
Patch Width ( $W$ )	4.2351
Substrate length ( $L_s$ )	5
Substrate width ( $W_s$ )	4.5
Feed line Insertion ( $F_i$ )	0.94
Feed line width ( $W_f$ )	0.3913



(a)



(b)



(c)

**Fig. 1 Configuration of proposed antenna (a) Geometry of the proposed antenna (b) 3D view (c) Simulation environment**

The Figure 1 (a) shows the proposed microstrip patch antenna which consists of overall geometry, figure 1(b) shows the 3 Dimensional view and figure 1 (c) shows the Simulation environment of a proposed antenna. This will easily help to identify the dimensional that were displayed in the table I. The proposed antenna was designed and simulated using CST Studio suite 2019 Simulation tools.

**Table III. Calculated Dimension of the Proposed slotted Antenna**

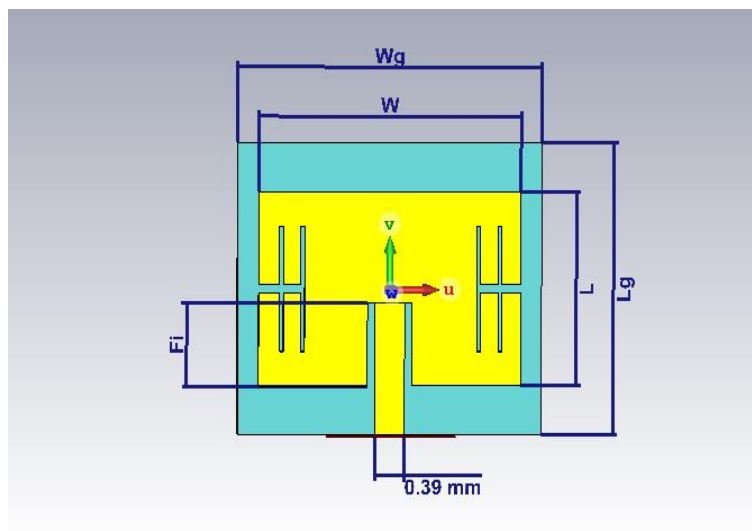
Parameters	Dimensions (mm)
Ground plane length (Lg)	4.9931
Ground plane Width (Wg)	5.7591
Patch length (L)	3.469
Patch Width (W)	4.235
Substrate length (Ls)	4.9931
Substrate Width (Ws)	5.7591
Feed line width (Wf)	0.39

**Table IV. Optimized Dimension of the Proposed slotted Antenna**

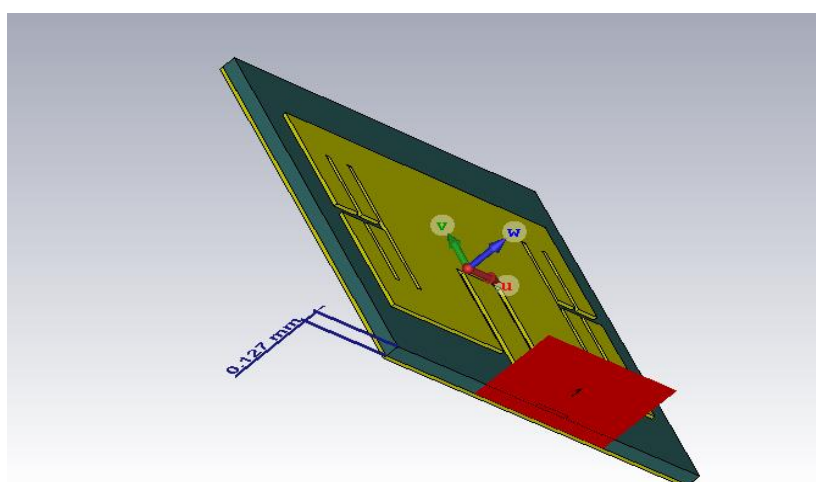
Parameters	Dimensions (mm)
Ground plane length (Lg)	3.5
Ground plane Width (Wg)	4
Patch length (L)	2.35
Patch Width (W)	3.45
Substrate length (Ls)	3.5
Substrate width (Ws)	4
Feed line Insertion (Fi)	1
Feed line width (Wf)	0.3913

**A. Antenna Design 2**

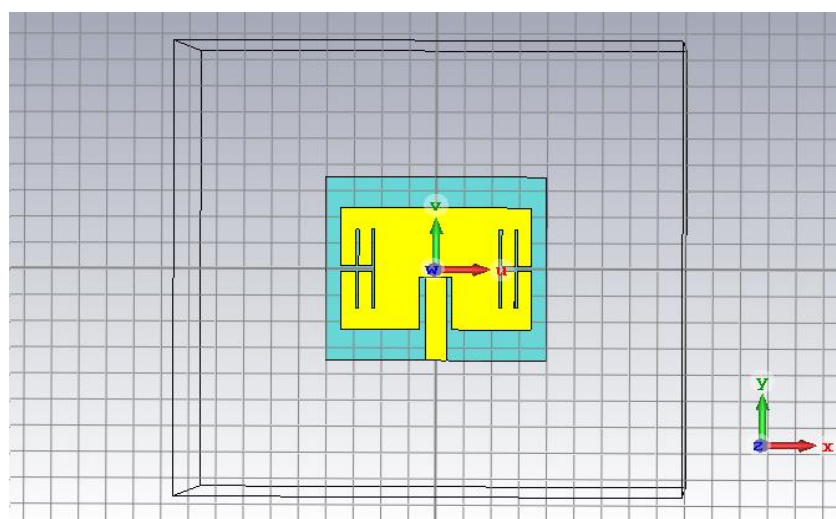
The proposed slotted microstrip antenna is operating under 28 GHz frequency band. The theoretical calculation and optimized values of the proposed microstrip patch antenna were listed in the table III and table IV. The Figure 2 (a) shows the proposed microstrip patch antenna which consists of overall geometry, figure 2 (b) shows the 3 Dimensional view and figure 2 (c) shows the Simulation environment of a proposed antenna. This will easily help to identify the dimensional that were displayed in the table I. The proposed antenna was designed and simulated using CST Studio suite 2019 Simulation tools.



(a)



(b)



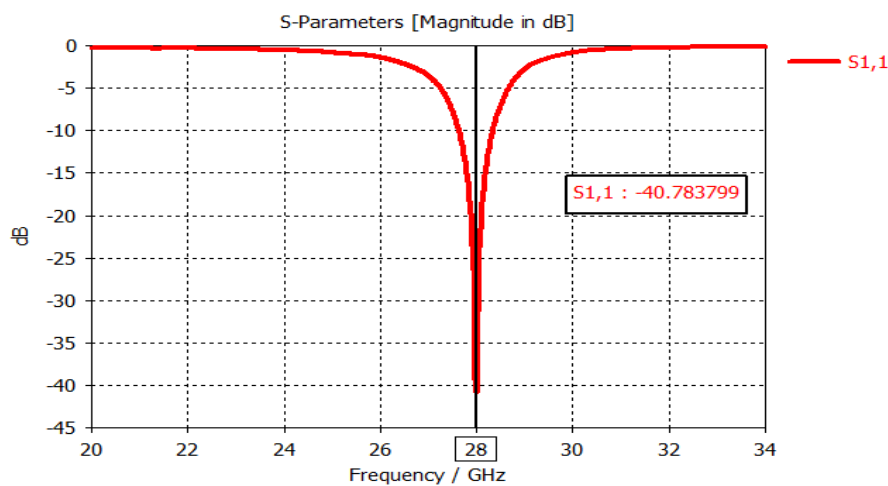
(c)

**Fig. 2 Configuration of proposed slotted antenna (a) Geometry of the proposed slotted antenna (b) 3D view (c) Simulation environment**

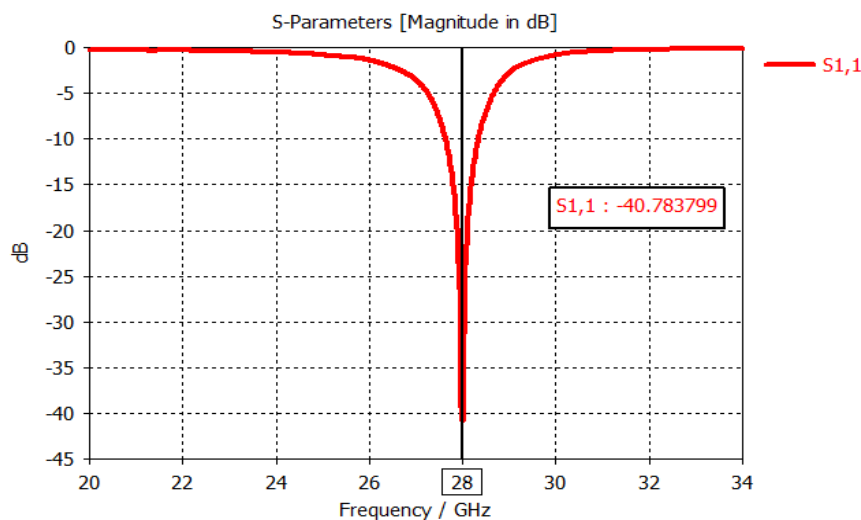
## SIMULATED RESULTS AND DISCUSSIONS

### B. Return loss

The return loss is one of the important parameters while designing a microstrip patch antenna. The accepted value of return loss should be less than -10 dB, which means 90 percent of the incident power will be accepted by the antenna to radiate and 10 percent of the power will be reflected back due to some discontinuity or miss matches in the design. The proposed antenna has a very good return loss of -40.7 dB without slot and 40 dB with slot respectively. The figure 3 (a) shows the return loss plot of the proposed antenna and figure 3 (b) shows the return loss plot of the proposed slotted microstrip antenna.



**Fig. 3 (a) Return loss plot of the proposed antenna**



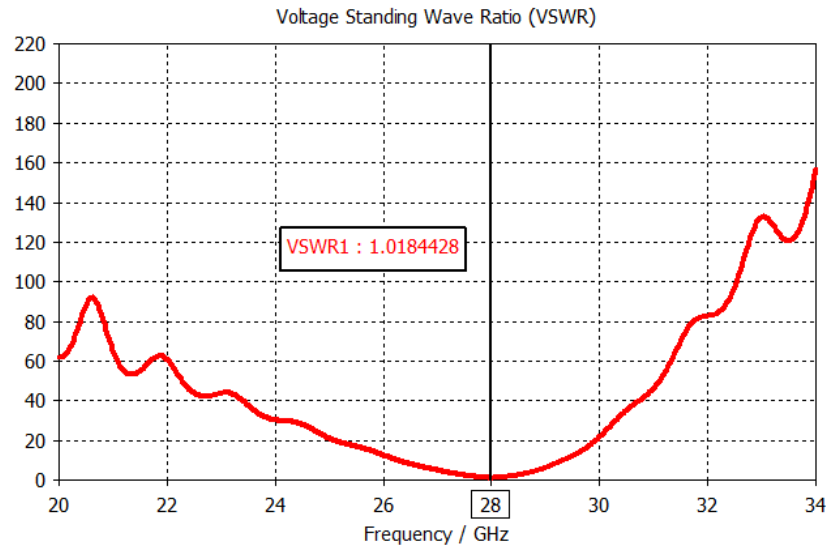
**Fig. 3 (b) Return loss plot of the proposed Slotted antenna**

### C. VSWR Plot

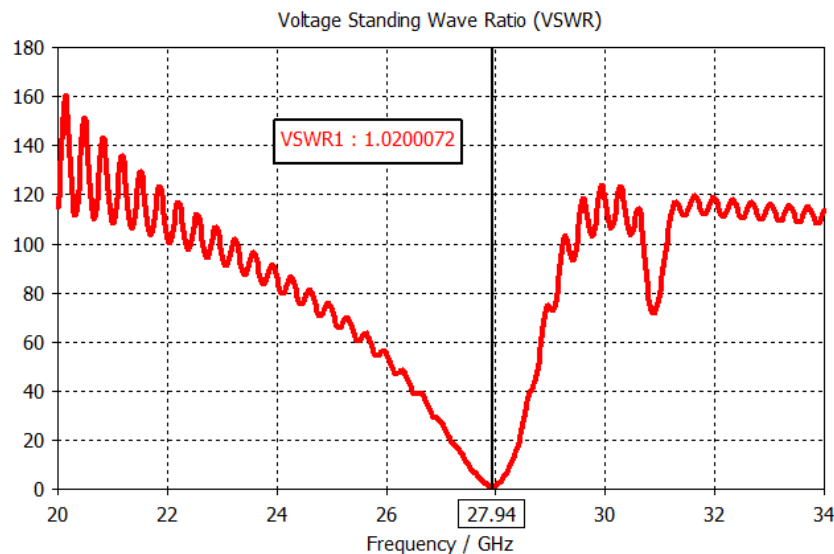
The Voltage standing wave ratio and the return loss will measure the reflected signal during transmission where VSWR measures the ratio of voltage applied to the reflected voltage. The proposed antenna has a better VSWR value of 1.0184428 at resonance



frequency of 28 GHz 27.94 GHz without slot and 1.0200072 at resonance frequency of 28 GHz 27.94 GHz with slot respectively. The figure 4 (a) shows the VSWR plot of the proposed antenna design and figure 4 (b) shows the VSWR plot of the proposed slotted antenna



**Fig. 4 (a) Voltage Standing Wave Ratio of the proposed antenna**

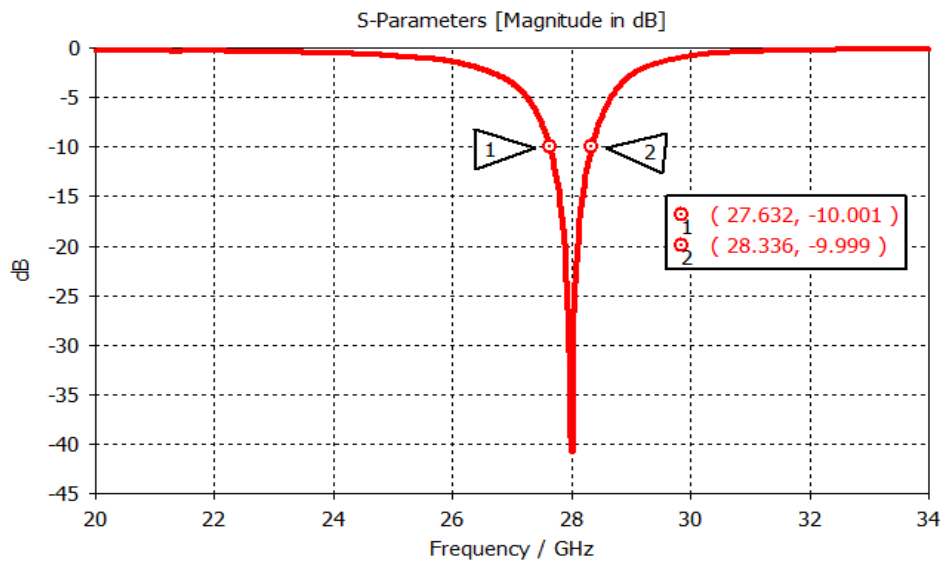


**Fig. 4 (b) Voltage Standing Wave Ratio of the proposed slotted antenna**

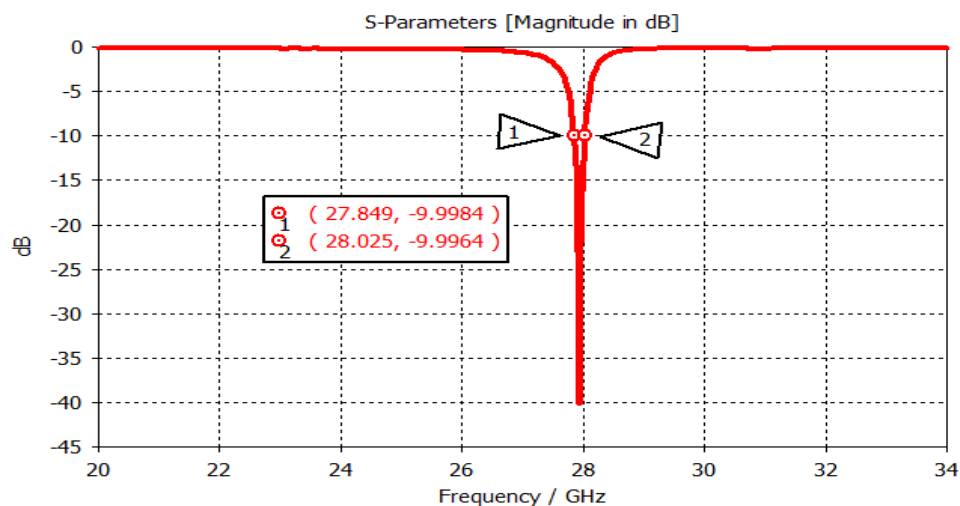
#### *D. Bandwidth*

Bandwidth is also one of the important antenna parameter. It shows the range of frequency over which the designed antenna can properly radiate or receive the electromagnetic energy. The bandwidth of the microstrip patch antenna is typically expresses in terms of VSWR. The VSWR is selected as the parameter for the bandwidth identification and thus this bandwidth is called the impedance bandwidth. Figure 5 (a) shows that the proposed microstrip patch antenna has the 10dB impedance bandwidth ranging from 27.6 to 28.3 GHz and Figure 5 (b)

shows that the proposed slotted microstrip patch antenna has the 10 dB impedance bandwidth ranging from 27.8 to 28.025 GHz.



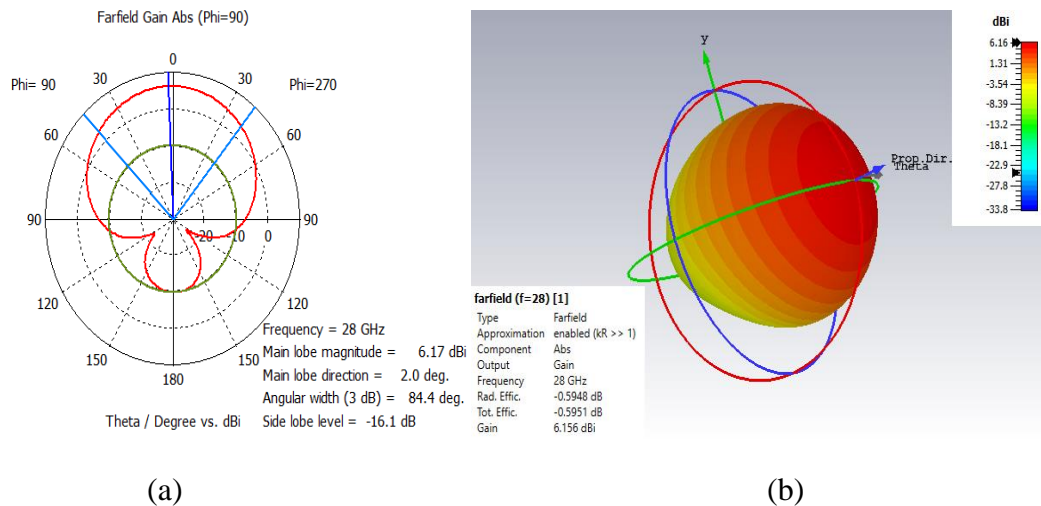
**Fig. 5 (a) Bandwidth plot of the proposed antenna**



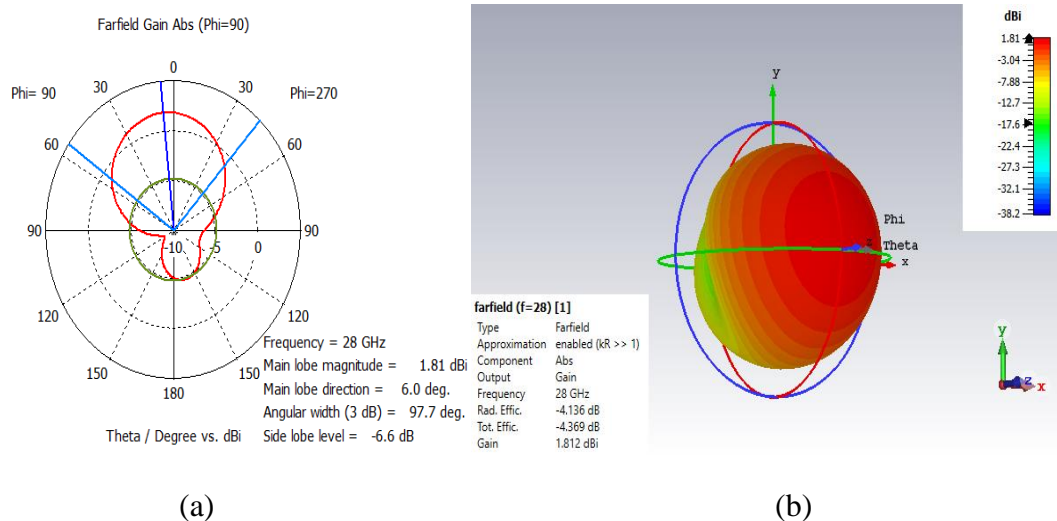
**Fig. 5 (b) Bandwidth plot of the proposed slotted antenna**

*E. Gain*

The antenna gain radiation pattern shows that the antenna radiate more or less in any direction. Typically, is the overall performance of an antenna, which is the combination of antenna directivity and efficiency. The figure 6 shows the 2D and 3D gain radiation pattern of the proposed microstrip patch antenna, it has a gain of 6.156 dBi and the Half Power Beam Width of 84.4° along with the side lobe level of -16.1 dB and the figure 7 shows the 2D and 3D gain radiation pattern of the proposed microstrip patch antenna, it has a gain of 1.812 dBi and the Half Power Beam Width of 97.7° along with the side lobe level of -6 dB respectively.



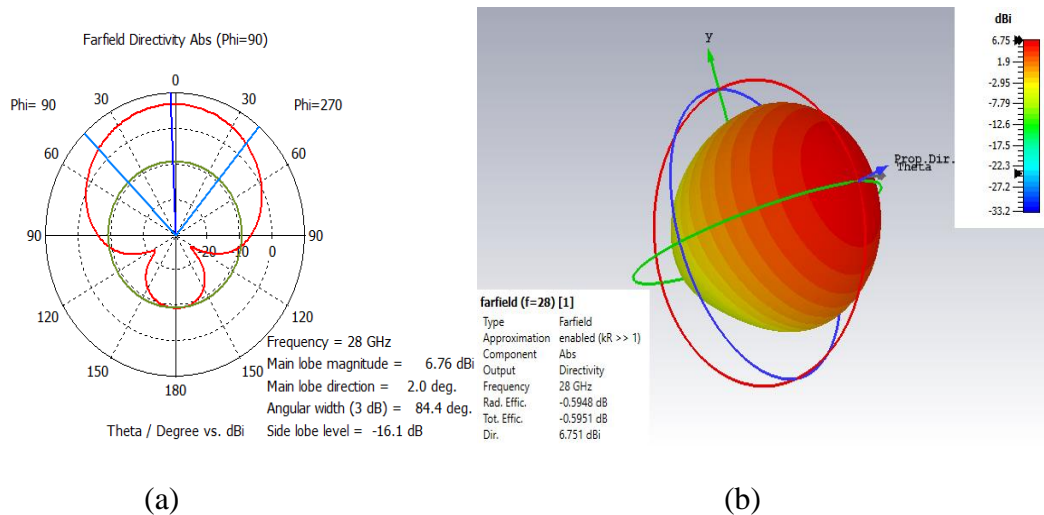
**Fig. 6 (a) 2D gain polar plot of the proposed antenna (b) 3D gain radiation pattern of the proposed antenna**



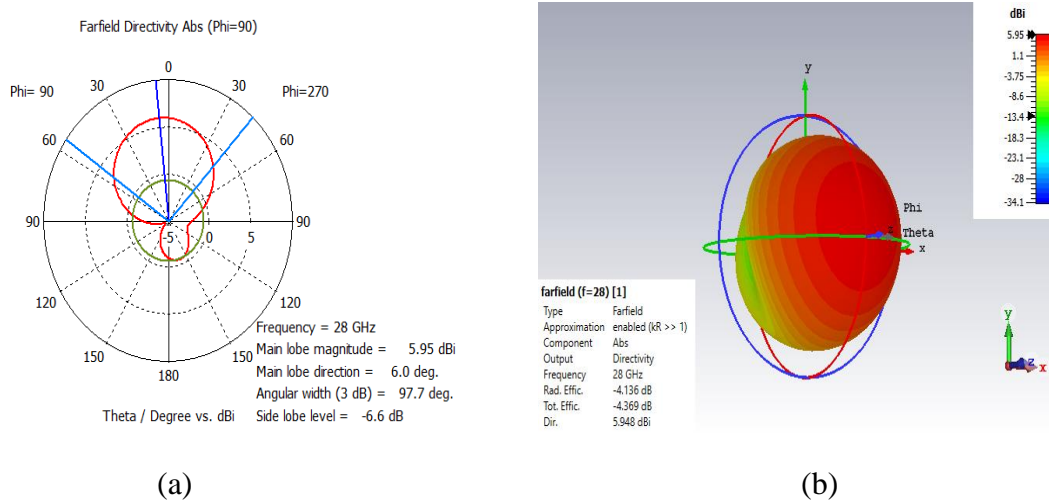
**Fig. 7 (a) 2D gain polar plot of the proposed slotted antenna (b) 3D gain radiation pattern of the proposed slotted antenna**

*F. Directivity*

Directivity is also one of the important antenna parameters. It is a measure of how the directional of an antenna is to be. The capacity of an antenna to focus its power or beam in a particular direction or receives the electromagnetic energy. The figure 8 shows the 2D and 3D gain radiation pattern of the proposed microstrip patch antenna, it has a gain of 6.751 dBi and the Half Power Beam Width of 84.4° along with the side lobe level of -16.1 dB and figure 9 shows the 2D and 3D gain radiation pattern of the proposed microstrip patch antenna, it has a gain of 5.948 dBi and the Half Power Beam Width of 97.7° along with the side lobe level of -6.6 dB.



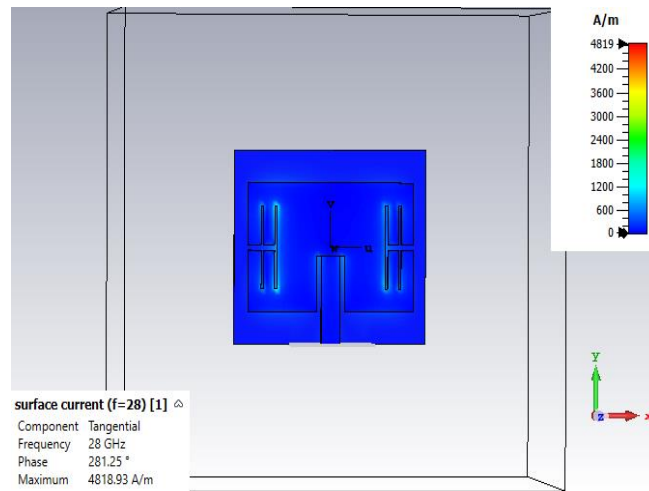
**Fig. 8 (a) 2D Directivity polar plot of the proposed antenna (b) 3D Directivity radiation pattern of the proposed antenna**



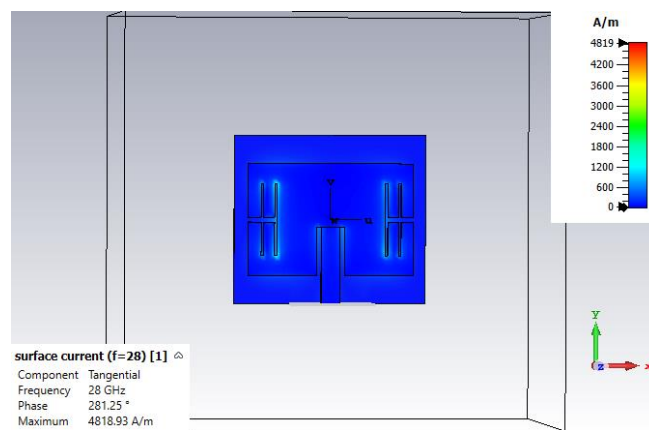
**Fig. 9 (a) 2D Directivity polar plot of the proposed slotted antenna (b) 3D Directivity radiation pattern of the proposed slotted antenna**

### G. Surface current

The figure 10 (a) shows the surface current of the proposed microstrip patch antenna and figure (b) 10 shows the surface current of the proposed slotted microstrip patch antenna.



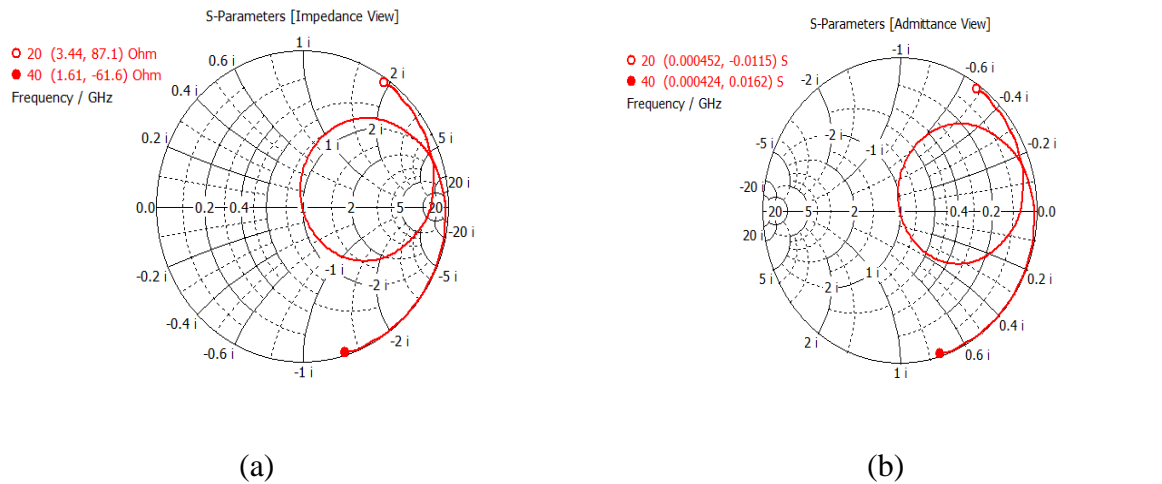
**Fig. 10 (a) Surface Current Distribution of the proposed antenna**



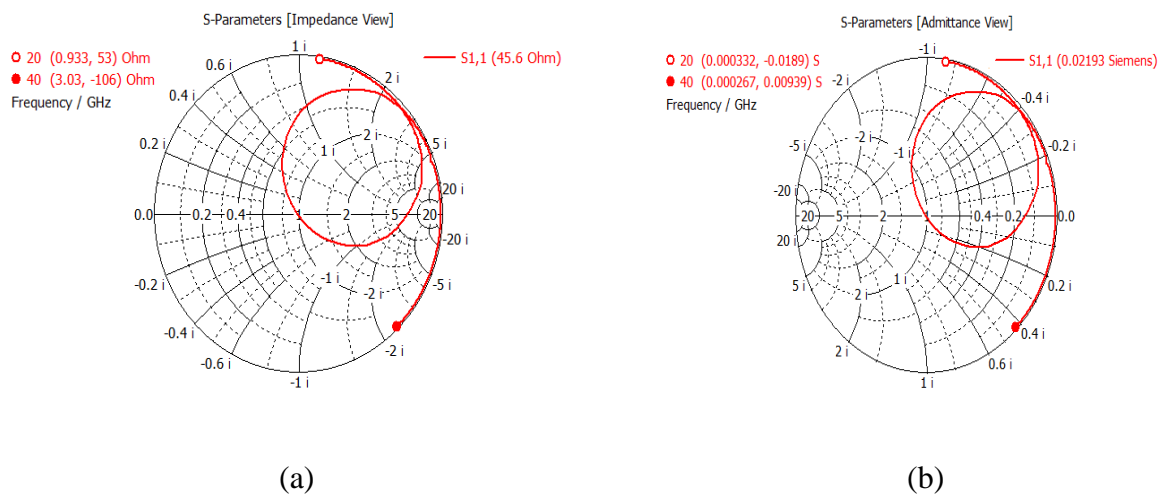
**Fig. 10 (b) Surface Current Distribution of the proposed slotted antenna**

### *H. Smith Chart*

The figure 11 shows the impedance and the admittance smith chart of the proposed microstrip antenna and figure 12 shows the impedance and the admittance smith chart of the proposed slotted microstrip antenna



**Fig. 11 (a) Impedance chart of the proposed antenna (b) Admittance chart of the proposed antenna**



**Fig. 12 (a) Impedance chart of the slotted antenna (b) Admittance chart of the slotted antenna**

## RESULT COMPARISONS

The table III and IV shows the overall simulated results of the proposed microstrip patch antenna and the comparison of the proposed antenna with the published result. This comparisons shows that the proposed microstrip patch antenna has overall dimension of  $5 \times 4.5 \times 0.254$  mm, which is suitable for the future generation compact wireless devices.

**Table V Overall simulated results**

Parameter	Without slot	With slot
Dimensions (mm)	5×4.5×0.127	3.5×4×0.127
Resonant frequency (GHz)	28	27.94
Return loss (dB)	-40.7	-40
VSWR	1.0184428	1.0200072
Bandwidth (GHz)	0.7	0.5
Gain (dBi)	6.156	1.812
Directivity (dBi)	6.751	5.948

**Table VI Comparison of the proposed antenna with the published work**

Published antenna	Dimensions(mm)	Return loss (dB)	Gain (dBi)	Bandwidth (GHz)
[1]	4.5×5.5×0.4	-17.4	6.72	1.1
[2]	6.285×7.235×0.5	-13.48	6.63	0.847
[3]	8.5×8.5×0.325	-54.4	7.55	1.062
[4]	6.20×8.40×1.57	-22.5	5.06	5.57
[5]	7×7×0.8	-27.79	6.59	2.62
[6]	7×7×0.8	-39.3	6.37	2.48
[7]	14.71×7.9×0.254	-12.59	6.69	0.582
[8]	10×8×1.6	-40.14	5.29	14.674
[9]	8.5×8.5×0.244	-38.86	7.587	1.046
[10]	7.14×8.52×0.12	-36.179	6.72	0.463
<b>This work Without slot</b>	<b>5×4.5×0.127</b>	<b>-40.7</b>	<b>6.156</b>	<b>0.7</b>
<b>This work with slot</b>	<b>3.5×4×0.127</b>	<b>-40</b>	<b>1.812</b>	<b>0.5</b>

## CONCLUSION

This research work mainly focuses on the design of compact microstrip patch antenna for future generation wireless communications. To reduce the size of an antenna, a trade off should be made with other parameters such as impedance bandwidth, the gain of an antenna and the directivity. The size of an antenna will be reduced by increasing the substrate thickness, it will increase the impedance bandwidth but it will decrease the antenna gain and directivity. Also the volume of an antenna should be considered. This work gives the overall compact size of overall dimension of  $5 \times 4.5 \times 0.127$  mm (without slot) and  $3.5 \times 4 \times 0.127$  mm (with slot), which is suitable for the future generation compact wireless devices and IoT applications. Further, an array of an antenna will be developed for the 5G MIMO (Multiple Input and Multiple Output), Beamforming and Small cell technologies. The 5G wireless communication widely depends on the MIMO antennas.

## REFERENCES

- [1] R. K. Goyal and U. Shankar Modani, "A Compact Microstrip Patch Antenna at 28 GHz for 5G wireless Applications," 2018 3rd International Conference and Workshops on Recent Advances and Innovations in Engineering (ICRAIE), 2018, pp. 1-2, doi: 10.1109/ICRAIE.2018.8710417.
- [2] Omar Darbo, Dominic Bernard, Onyango Konditi, and Faranklin Manene, "A 28 GHz Rectangular Microstrip Patch Antenna for 5G Application," International Journal of Engineering and Technology, Vol. 12, no. 6, pp. 854-857, (2019).
- [3] Kinde Anlay Fante and Mulugeta Tegegn Gameda, "Broadband microstrip patch antenna at 28 GHz for 5G wireless applications," International Journal of Electrical and Computer Engineering (IJECE) Vol. 11, No. 3, June 2021, pp. 2238-2244.
- [4] R. Przesmycki, M. Bugaj, and L. Nowosielski, "Broadband microstrip antenna for 5G wireless systems operating at 28 GHz," Electronics, vol. 10, pp. 1, 2021, <https://dx.doi.org/10.3390/electronics10010001>.
- [5] P. Merlin Teresa and G. Umamaheswari, 2020, "Compact Slotted Microstrip Antenna for 5G Applications Operating at 28 GHz," IETE Journal of Research, doi: 10.1080/03772063.2020.1779620.
- [6] A. F. Kaeib, N. M. Shebani and A. R. Zarek, "Design and Analysis of a Slotted Microstrip Antenna for 5G Communication Networks at 28 GHz," 2019 19th International Conference on Sciences and Techniques of Automatic Control and Computer Engineering (STA), pp. 648-653, doi: 10.1109/STA.2019.8717292.
- [7] S. Mungur Dheeraj and Shankar Duraikannan, "Microstrip Patch Antenna at 28 GHz for 5G Applications," Journal of Science Technology Engineering and Management Advanced Research & Innovation, vol. 1, no. 1, pp. 5-7, 2018.
- [8] Shehab Khan Noor<sup>1</sup>, Nurulazlina Ramli<sup>1</sup>, Norsuzlin Mohd Sahar and Taher Khalifa<sup>1</sup>, 2021, "Compact and Wide Bandwidth Microstrip Patch Antenna for 5G Millimeter Wave Technology: Design and Analysis," Journal of Physics: Conference Series. 1878(2021)012008, doi: 10.1088/1742-6596/1878/1/012008.
- [9] Mulugeta Tegegn Gameda<sup>1</sup>, Kinde Anlay Fante, Hana Lebeta Goshu, Ayane Lebeta Goshu, 2021, "Design and Analysis of a 28 GHz Microstrip Patch Antenna for 5G Communication Systems," International Research Journal of Engineering and Technology (IRJET), VOLUME 8. 881-886.



- [10] M Darsono and A R Wijaya, "Design and simulation of a rectangular patch microstrip antenna for the frequency of 28 GHz in 5G technology," IOP Conf. Series: Journal of Physics: Conf. Series, 1469 (2020) 012107, doi:10.1088/1742-6596/1469/1/012107.
- [11] Deepika D Pai, "A Survey on Millimeter Wave Mobile Communications for 5G Cellular Networks," International Journal of Innovative Research in Electrical, Electronics, Instrumentation and Control Engineering, Vol. 5, Issue 6, June 2017, doi: 10.17148/IJIREEICE.2017.5647.
- [12] J. D. Terán Guerra a, E. A. Morales González a, F. López Huerta a, I. Arceo Rosas b, R. López Leal d, J. Martínez Castillo, "Patch antenna design for UHF RFID systems," 2019 IEEE International Conference on Engineering Veracruz (ICEV), 2019, pp. 1-5, 2019.
- [13] António Morgado, Kazi Mohammed Saidul Huq, Shahid Mumtaz and Jonathan Rodriguez, "A survey of 5G technologies: regulatory, standardization and industrial perspectives," Digital Communications and Networks, 2018, <https://doi.org/10.1016/j.dcan.2017.09.010>, Volume 4, Issue 2, pp. 87-97.
- [14] Ahmed Al-Saman 1, Michael Cheffena, Olakunle Elijah, Yousef A. Al-Gumaei, Sharul Kamal Abdul Rahim and Tawfik Al-Hadhrami, "Survey of Millimeter-Wave Propagation Measurements," Electronics 2021, 10, 1653. <https://doi.org/10.3390/electronics10141653>.
- [15] A. N. Uwaechia and N. M. Mahyuddin, "A Comprehensive Survey on Millimeter Wave Communications for Fifth-Generation Wireless Networks: Feasibility and Challenges," in IEEE Access, vol. 8, pp. 62367-62414, 2020, doi: 10.1109/ACCESS.2020.2984204.
- [16] Adnan Majeed, "Comparative Studies of 3G, 4G & 5G Mobile Network & Data Offloading Method a Survey," IJRIT International Journal of Research in Information Technology, Volume 3, Issue 5, May 2015, Pg. 421-427.

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